



Docket No. 0188-06

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Bunji MIZUNO, et al. :
Serial No.: 09.840,306 : Group Art Unit: 1763
Filed: April 24, 2001 : Examiner:
For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Washington, DC 20231

Sir:

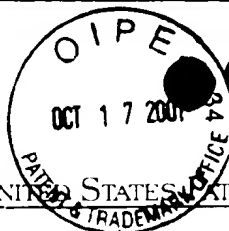
Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the number of total is incorrect on the filing receipt. Please correct the number of total claims to reflect: 38. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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UNITED STATES PATENT AND TRADEMARK OFFICE

 COMMISSIONER FOR PATENT
 UNITED STATES PATENT AND TRADEMARK OFFICE
 WASHINGTON, DC 20503

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE RECD	ATTY DOCKET NO	DRAWINGS	FIGURE SHEETS	NEW CLAIMS
09/840,306	04/24/2001	1763	1368	60188-063	10	36 38	6

CONFIRMATION NO. 4390

 20277
 MCDERMOTT WILL & EMERY
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UPDATED FILING RECEIPT



00000000685252

Date Mailed 10/05/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

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Domestic Priority data as claimed by applicant

 THIS APPLICATION IS A CIP OF 09/178,766 10/26/1998 ABN
 WHICH IS A DIV OF 08/734,218 10/21/1996 PAT 6,217,951

Foreign Applications

JAPAN 7-274234 10/23/1995

If Required, Foreign Filing License Granted 06/15/2001

Projected Publication Date: 01/10/2002

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing semiconductor device

Preliminary Class

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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